# Faculty CV

Name of Faculty: Shinde Mrunal Mukund

**Present Position:** Lecturer in ElectronicsEngineering

Government Polytechnic,

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## **QUALIFICATIONSDETAILS:**

Sr. No	Examination	Institute Name	Board/ University	% of Marks	Class Award
1	B.E.	WIT solapur	Shivaji university	72.27%	First class with distinction
2	PGDIP in International business	Symbiosis Pune	Symbiosis	66.50%	First class
3	M.E.	K.K.Wagh Nashik	Pune	73.50%	First class

#### **CAREER DETAILS:**

Sr. No.	Organization	Designation	Duration	Work Done in Brief
1	Minilec Pune	Electronics engineer	94-97	Production, Testing
2	G.P.Thane	Lecturer in Electronics Engineering	99-2000	Teaching and Administrative
3	Government Polytechnic, Nashik	Lecturer in Electronics Engineering	2001-10	Teaching and Administrative

4	Government Polytechnic, Jalgaon	Lecturer in Electronics Engineering	201012	Teaching and Administrative
5	Government Polytechnic, Nashik	Lecturer in Electronics Engineering	5/08/2012 till date	Teaching and Administrative

## **SPECIALIZED TRAINING COURSES ATTENDED- (Last 3 Years)**

Sr. No.	Course Title	Duration	Venue	Sponsored/ Approved by
1	Neural network and CNN	14/4/20 to 18/4/20	G.P. Nasik(Onli ne mode)	NITTTR Chandigarh
2	Outcome based curriculum design	1/6/20 to 5/6/20	G P.Nasik (Online mode)	NITTTR Chandigarh
3	IOT and Advanced power electronics applications in Smart grid	10/8/20 to 14/8/20	G P.Nasik (Online mode)	AICTE sponsored at K .K .W .Engg. Nasik
4	Cyber security and	21/12/22 to	Online mode	TNSCLC
	Ethical hacking	4/1/23		Hydrabad
5	Machine learning and its applications	24/1/23 to 7/2/23	Online mode	TNSCLC Hydrabad

## **RESEARCH ACTIVITIES:**

## PAPERS/JOURNALS PUBLISHED IN NATIONAL & INTERNATIONAL CONFERENCE:

List your publication details here.